



#4

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application Of:	Examiner: Unknown
Robert F. Terry	Art Unit: 2152
Serial No: 09/827,891	
Filed: April 6, 2001	
For: System And Method For Real Time Monitoring And Control Of Networked Computers	

Commissioner for Patents  
Washington, D.C. 20231

**LETTER TO THE CHIEF DRAFTSPERSON  
(Request to Substitute Drawings)**

Sir:

Subject to the approval of the Primary Examiner in the above-entitled patent application, please substitute the enclosed twenty-four (24) sheets of drawings, containing substituted figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, and 24, for the twenty-four (24) sheets of informal drawings containing Figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, and 24 as originally filed.

**REMARKS**

Applicant respectfully submits that the requested drawing substitution is consistent with the corresponding material in the specification and does not add any new matter to the application.

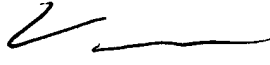
09827891-072701

Should the Examiner have any questions concerning this request, the Examiner is invited to call the undersigned at the number shown below.

Respectfully submitted,  
Robert F. Terry

Date: July 20, 2001

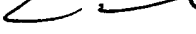
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Palo Alto, CA 94304-1043  
Telephone (650) 843-3397  
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By   
Arnold M. de Guzman  
Attorney for Applicant  
Registration No. 39,955

10/22/01 16B/2860

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231, on

Date: July 20, 2001 By:   
Arnold M. de Guzman

# System And Method For Real Time Monitoring And Control Of Networked Computer

Inventor: Robert F. Terry

Serial No.: 09/827,891

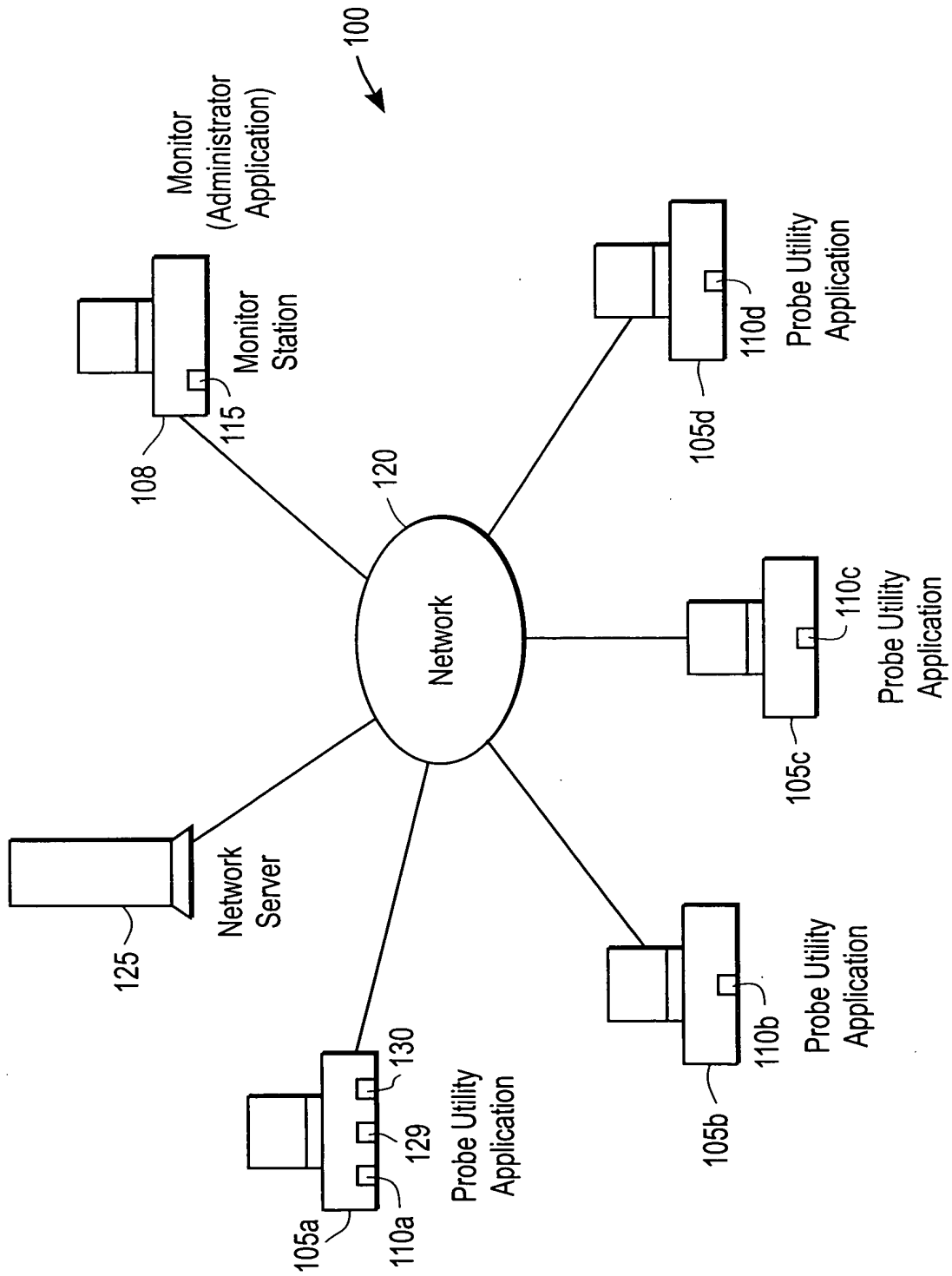


FIG. 1

10/22/01 16:28:50

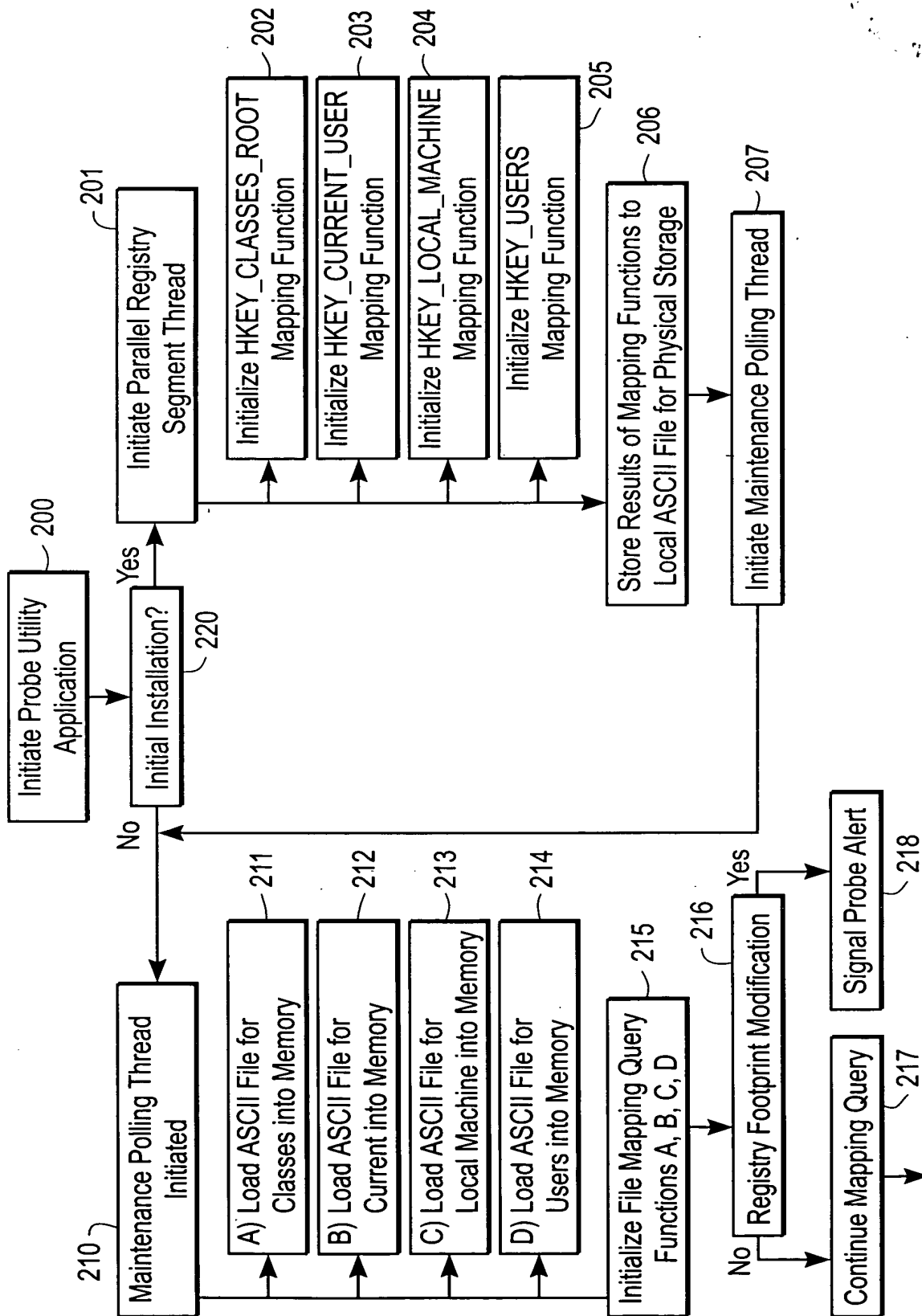


FIG. 2

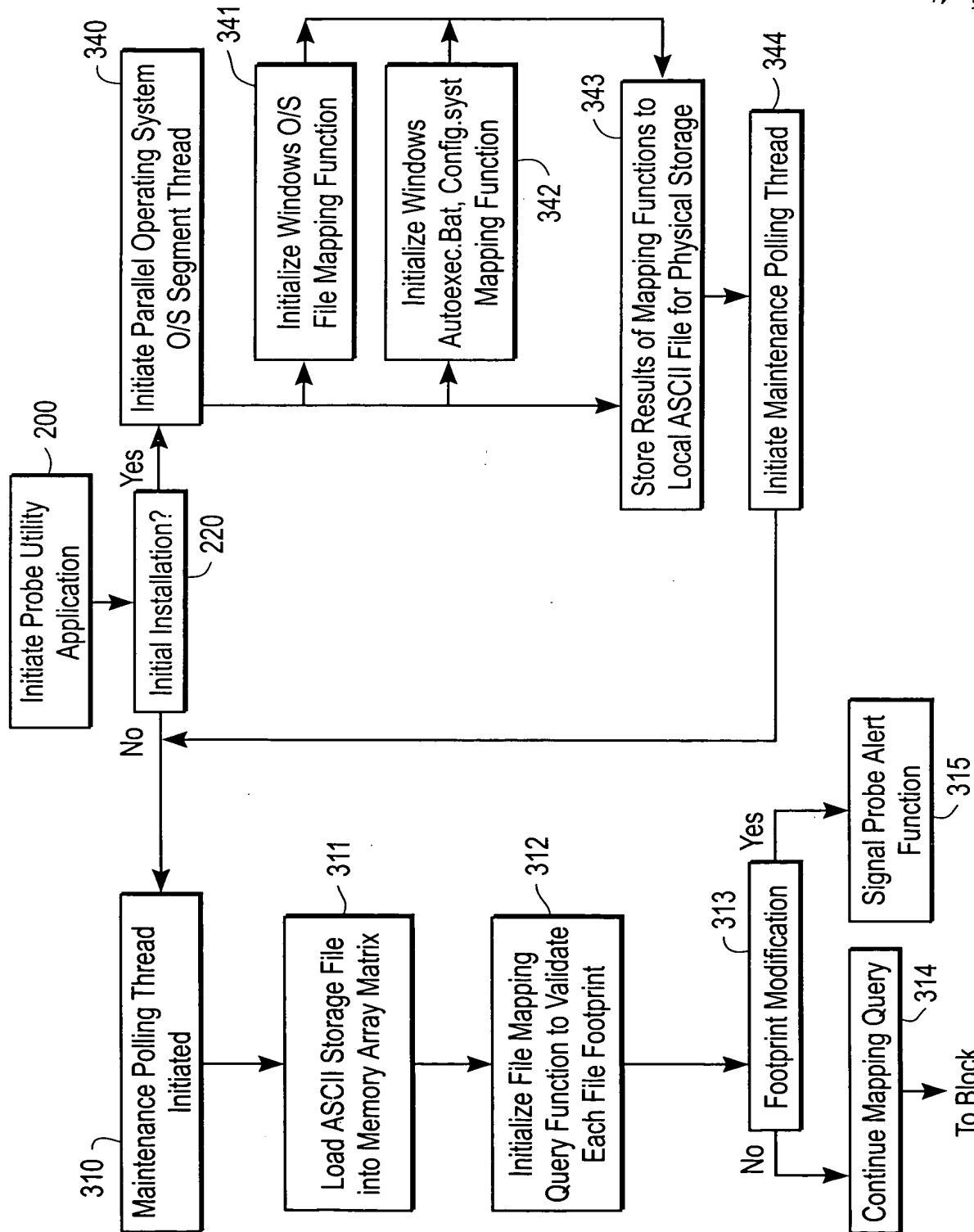


FIG. 3

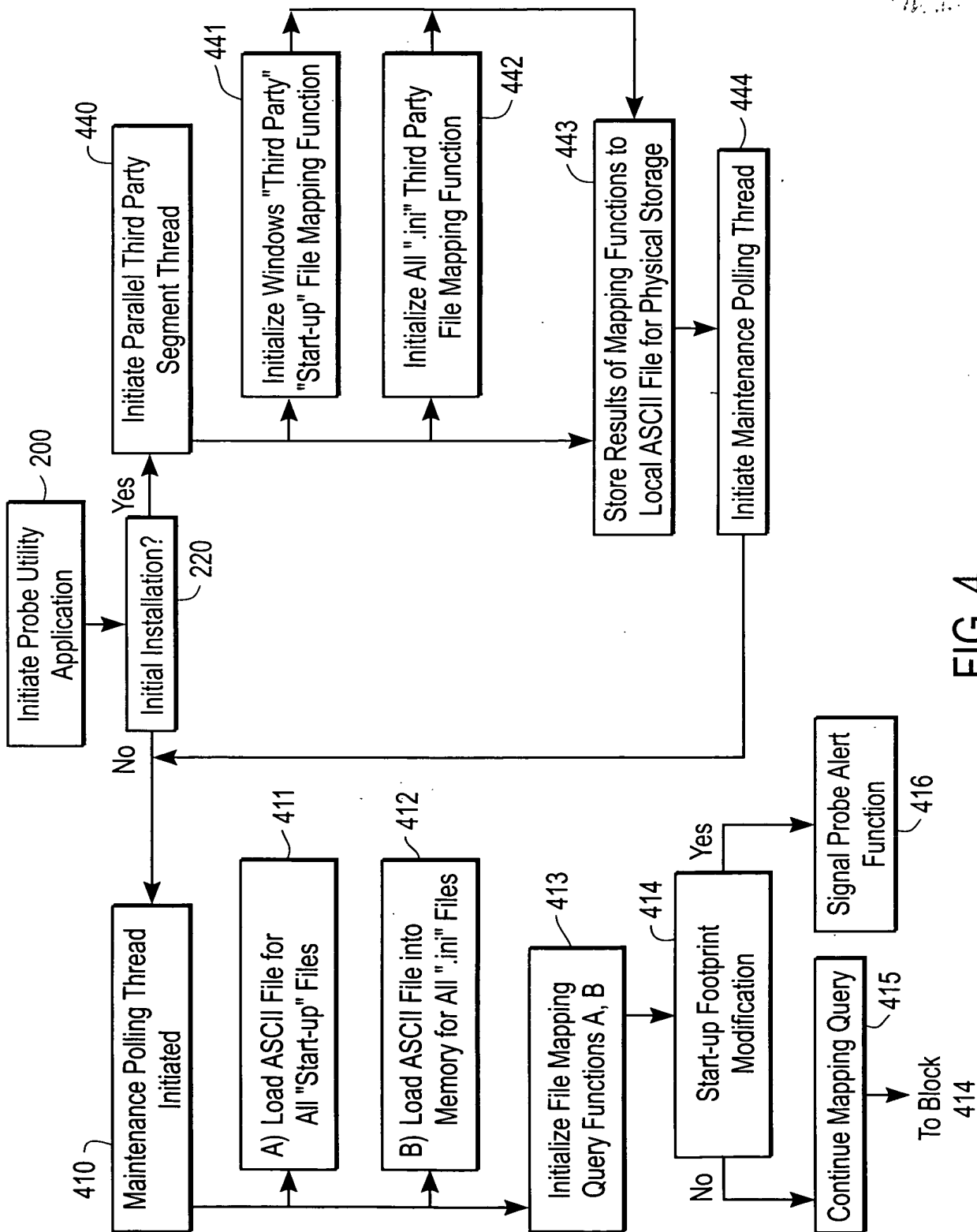


FIG. 4

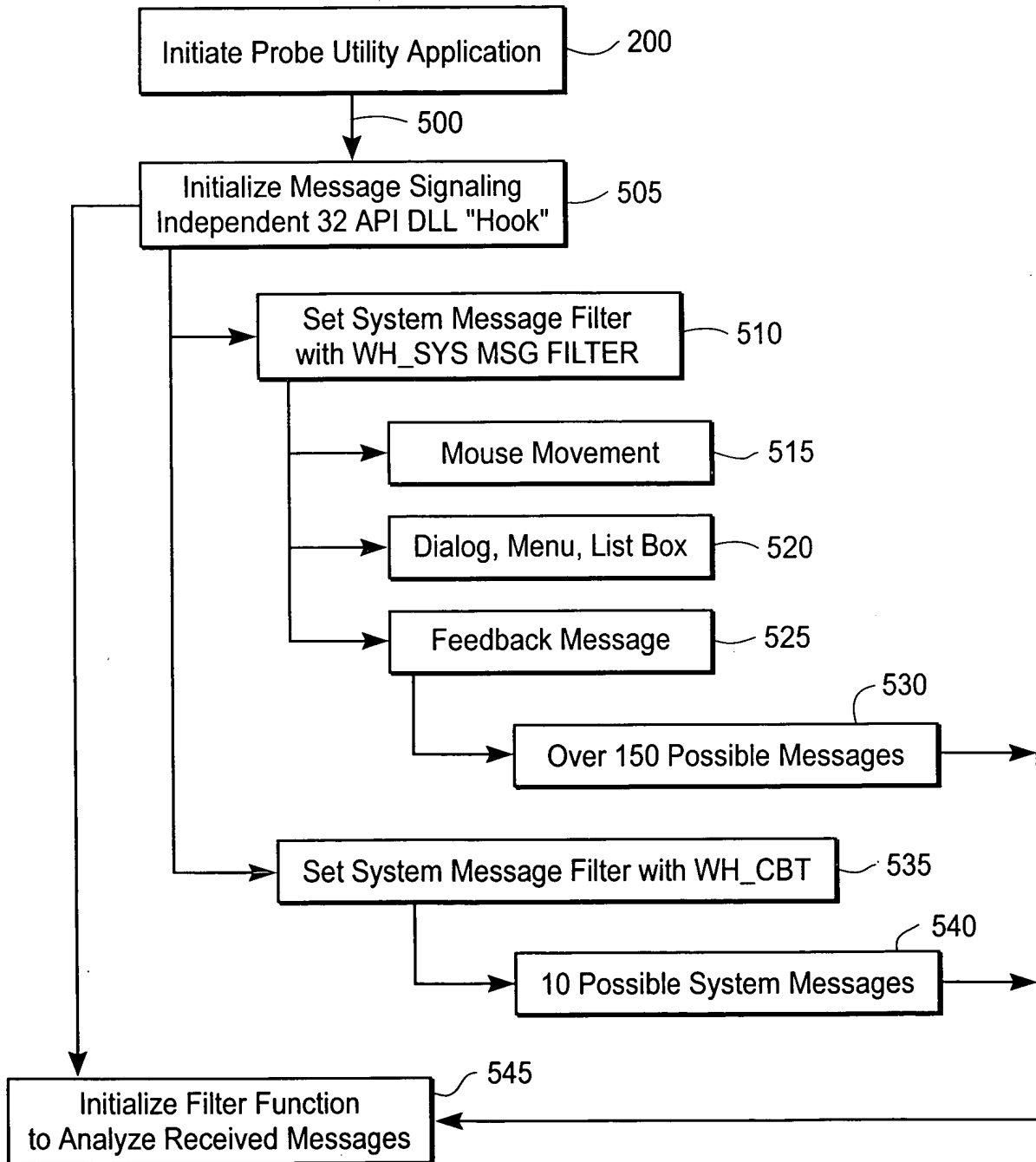


FIG. 5

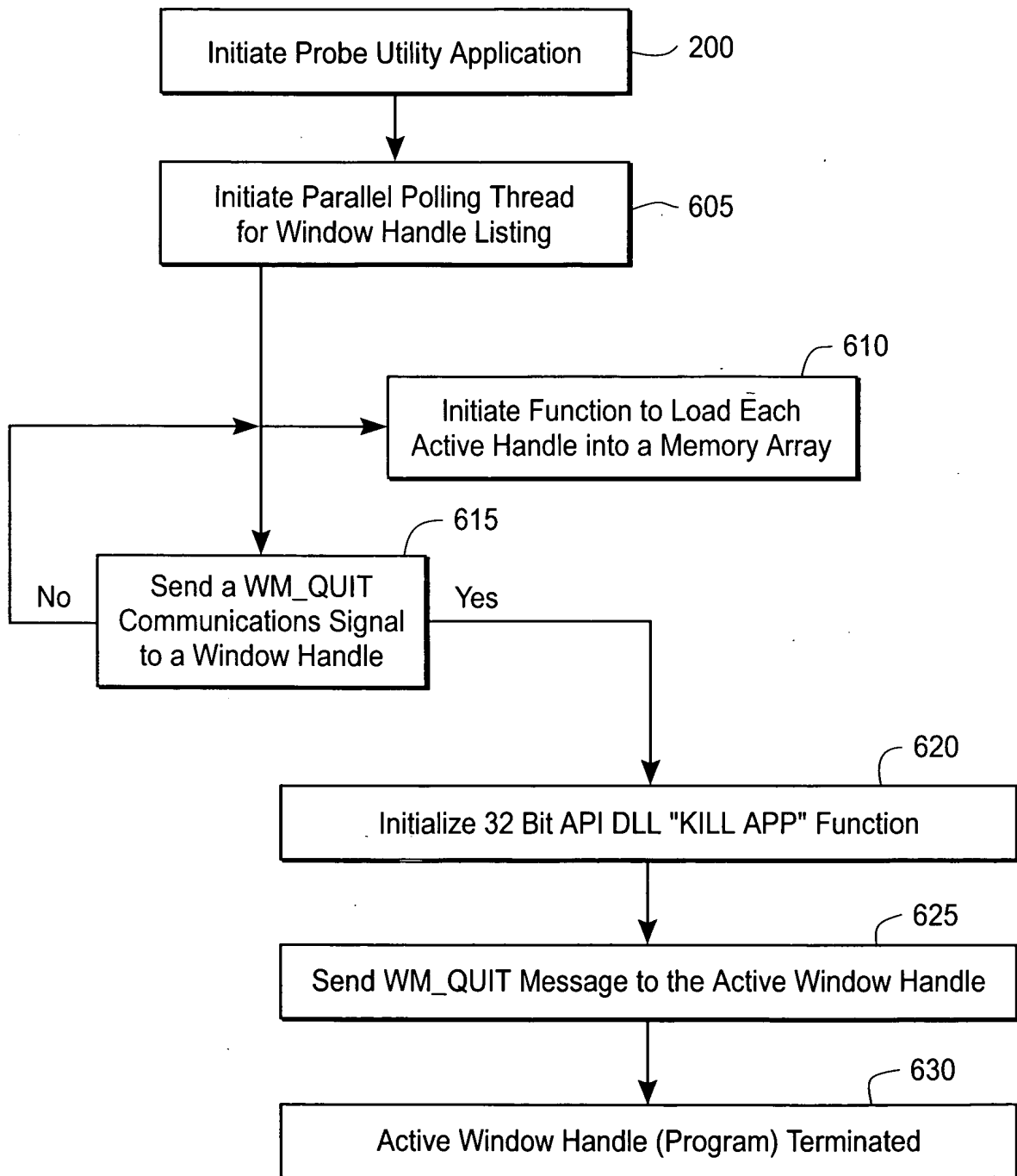


FIG. 6

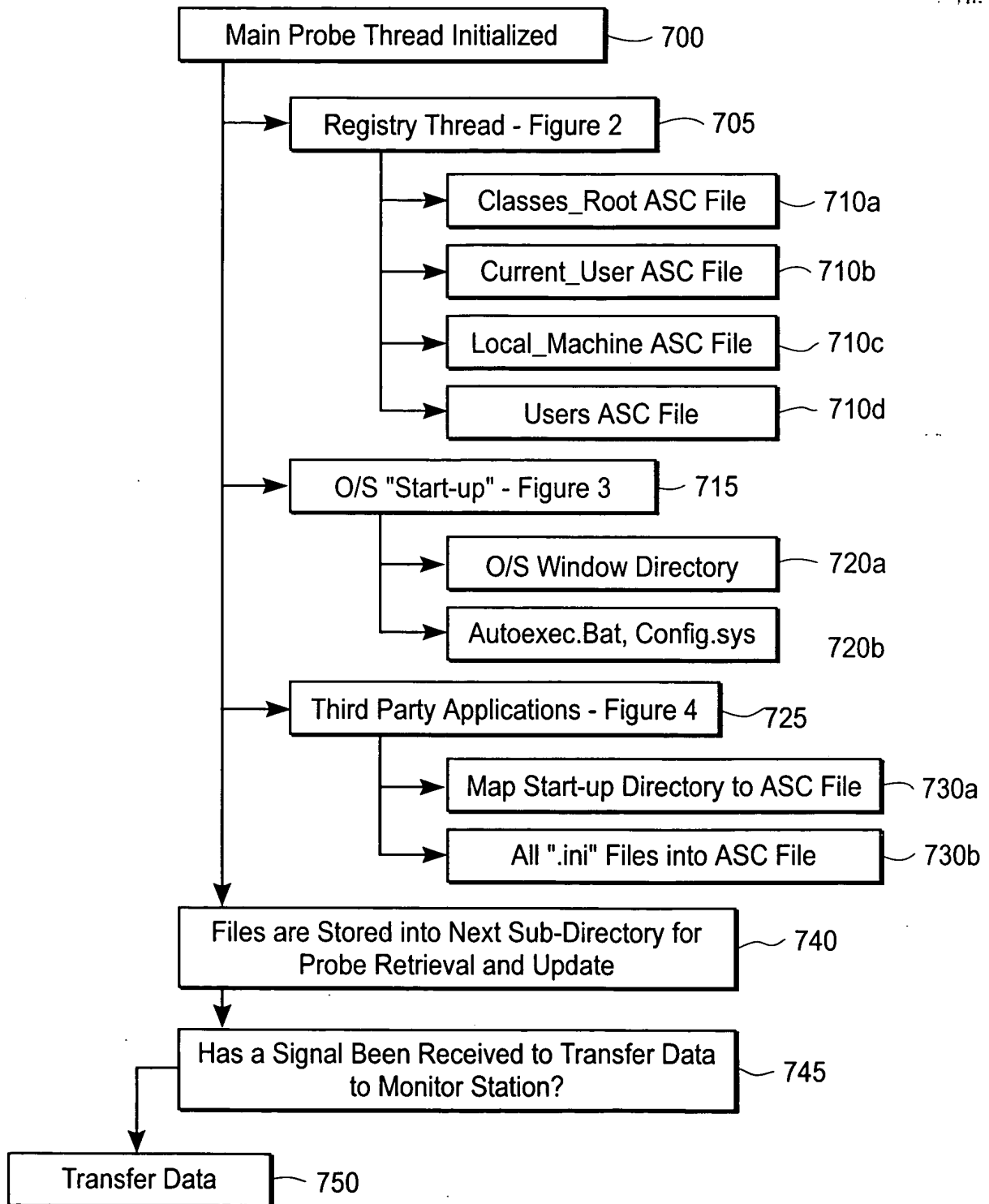


FIG. 7

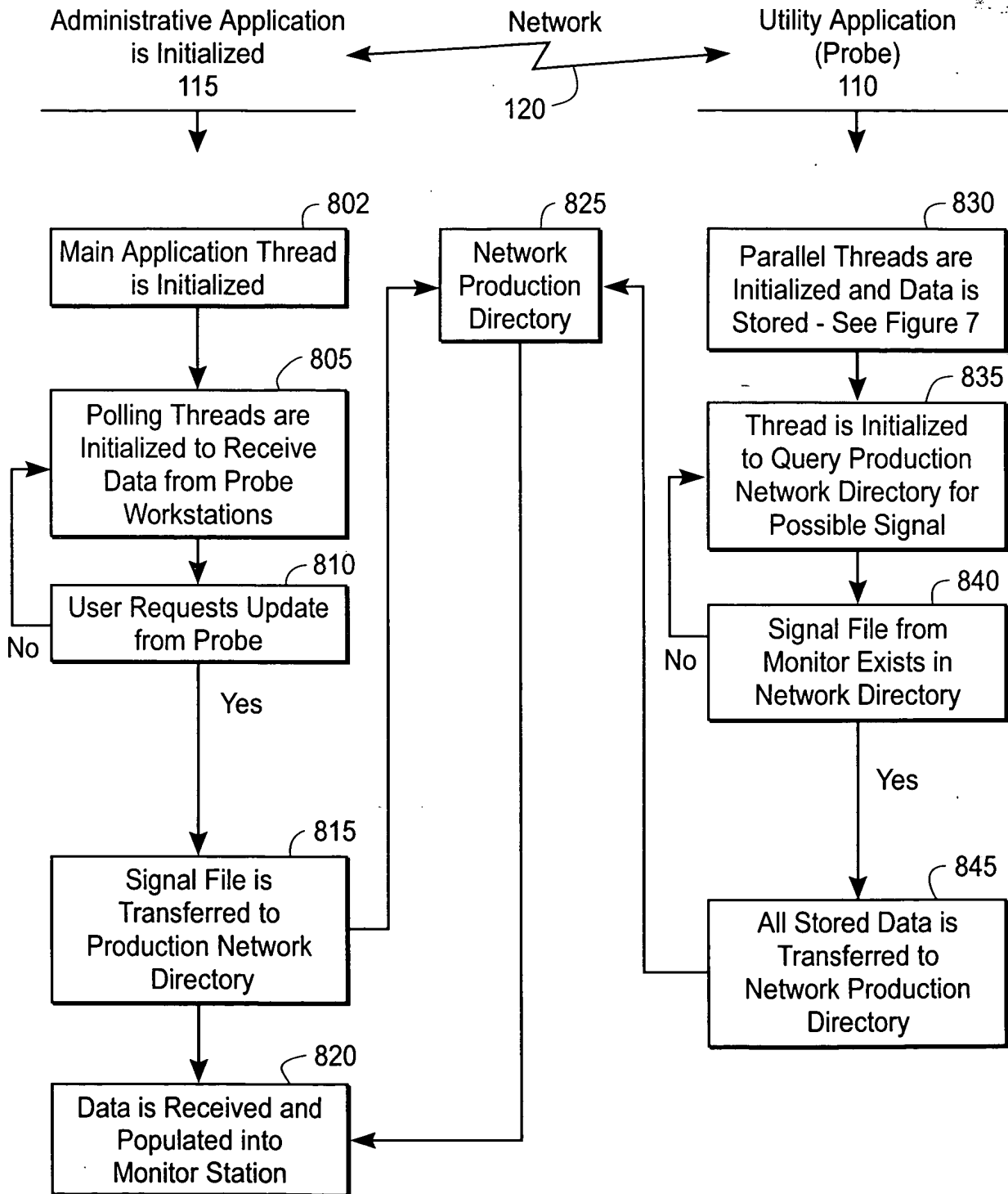


FIG. 8

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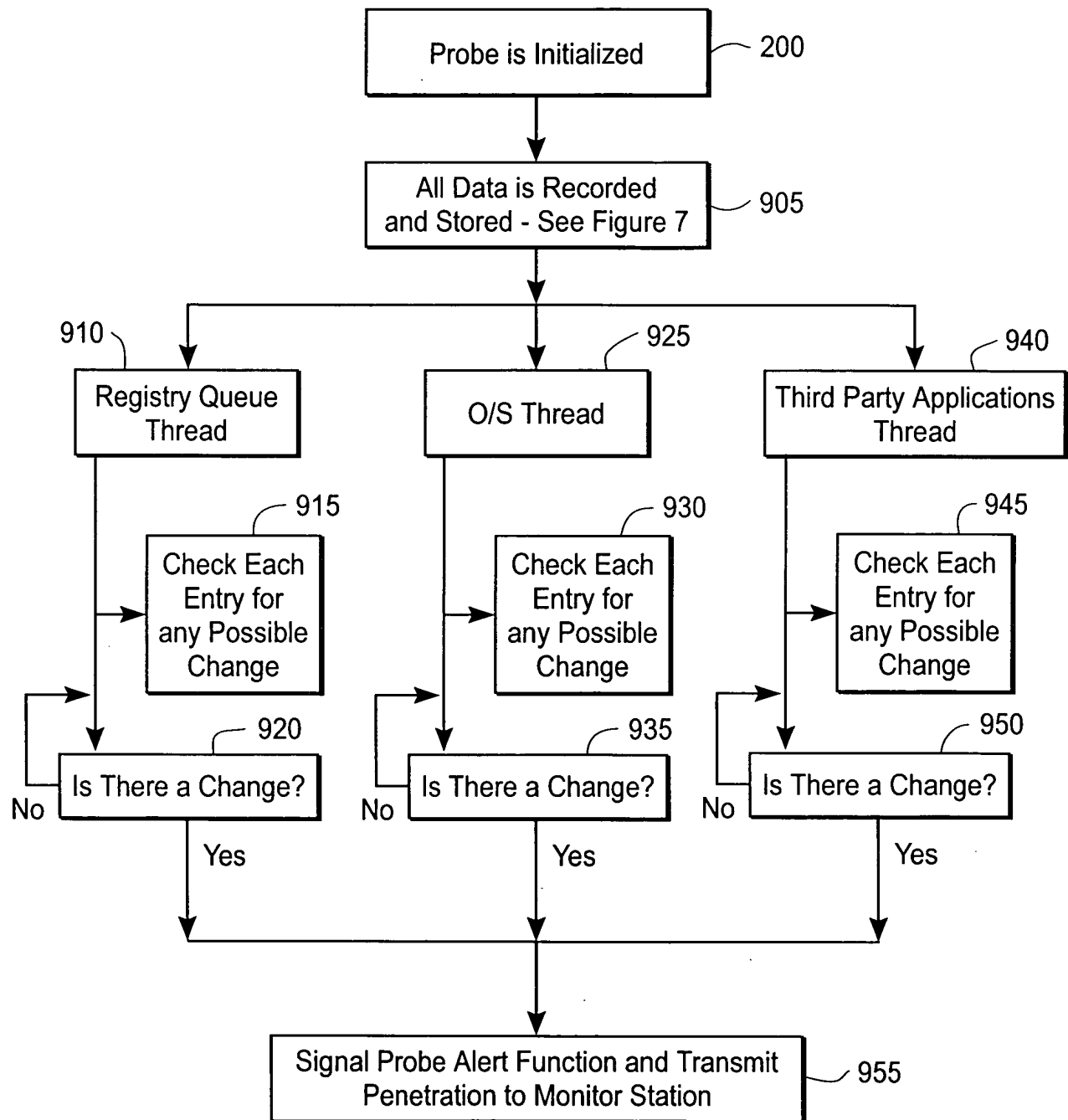


FIG. 9

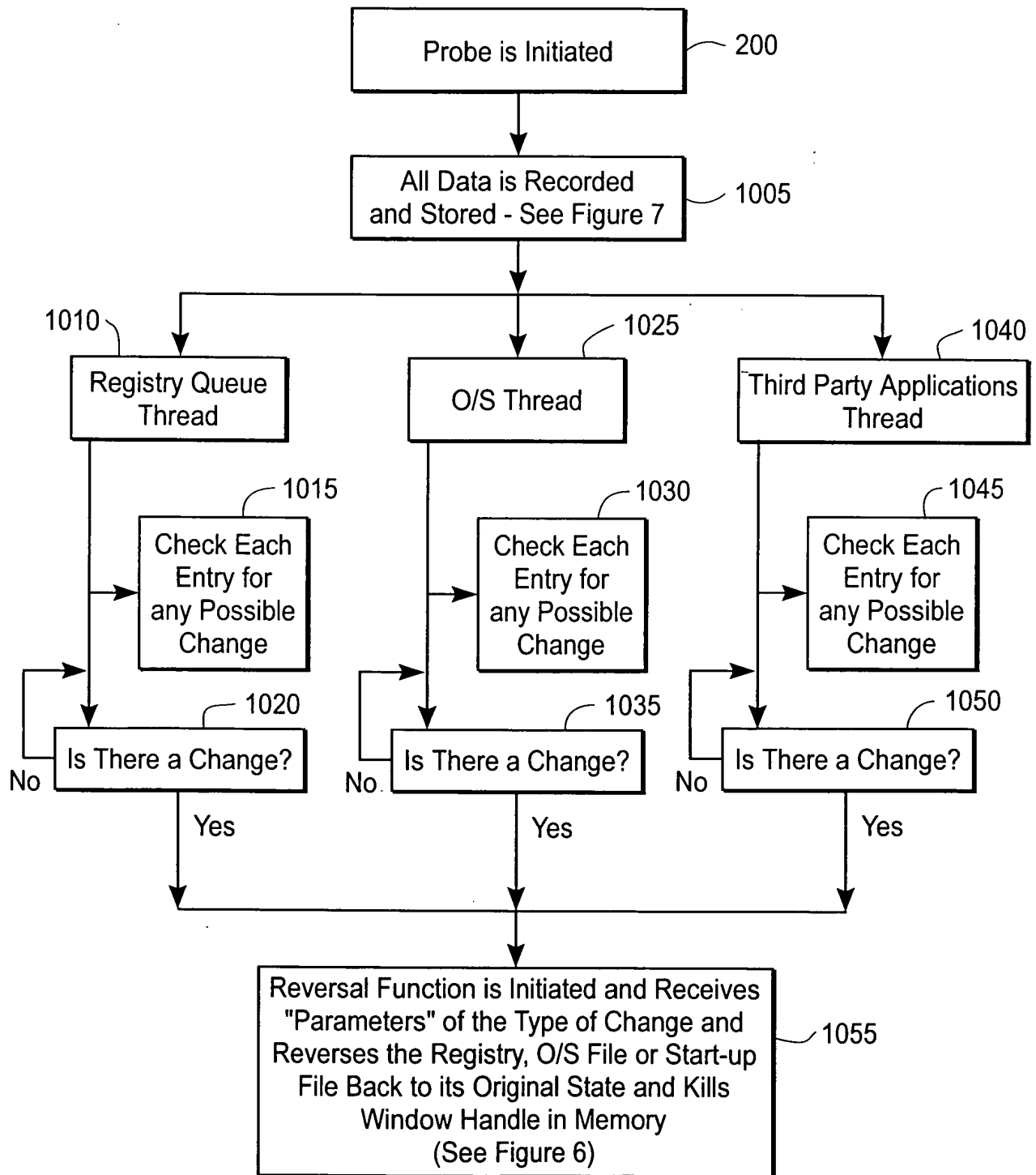


FIG. 10

1100

SOT [CR] [LF]  
Date=CCYY\MM\DD [cr] [lf]  
Time=HH : MM : SS [cr] [lf]  
3Wind=Variable Up To 500 Characters [cr] [lf]  
2Wind=Variable Up To 500 Characters [cr] [lf]  
1Wind=Variable Up To 500 Characters [cr] [lf]  
Mssg=Variable Up To 500 Characters [cr] [lf]  
EOT [cr] [lf]

Structured Signal File Block Diagram

FIG. 11

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TD220-T582850

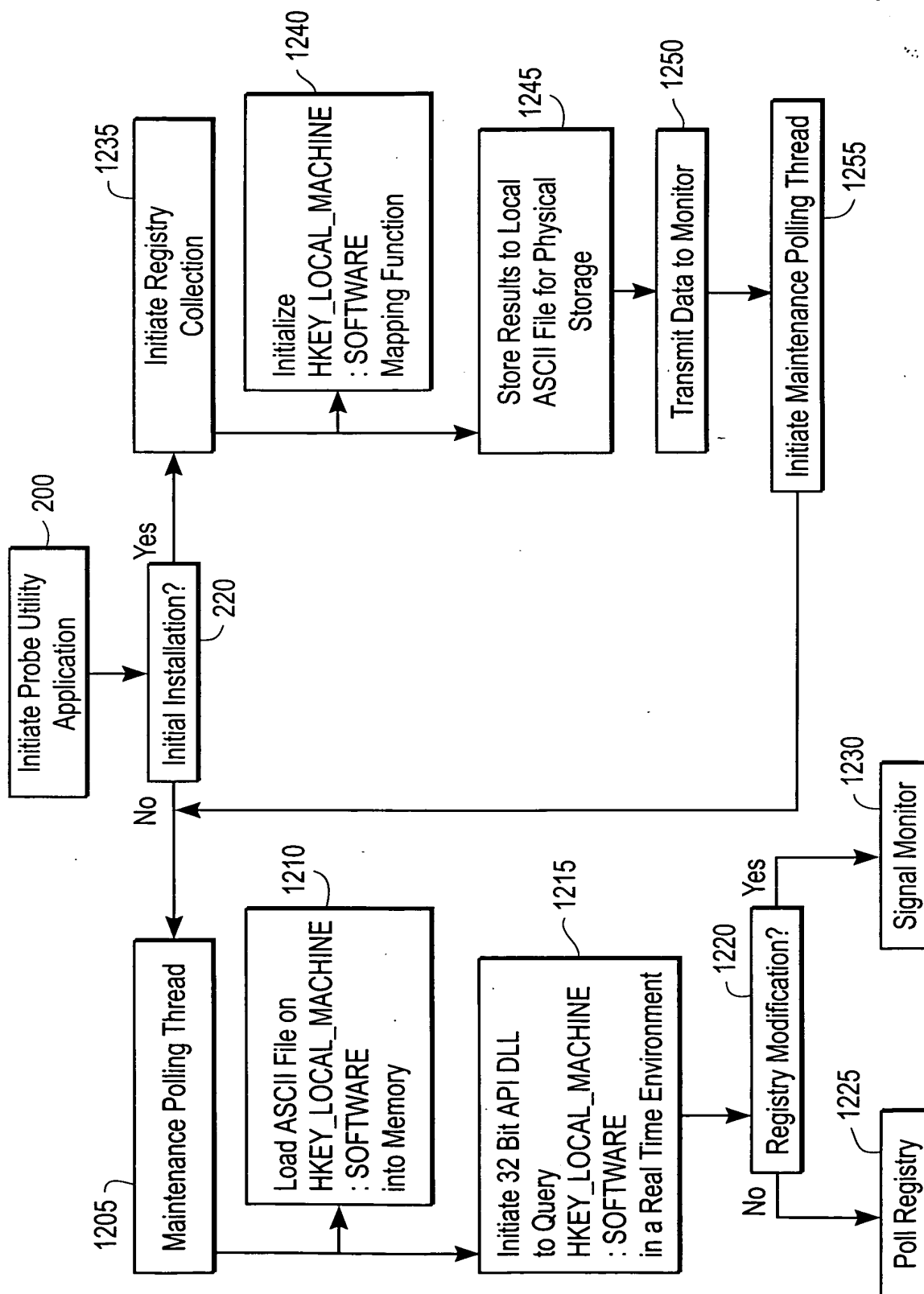


FIG. 12

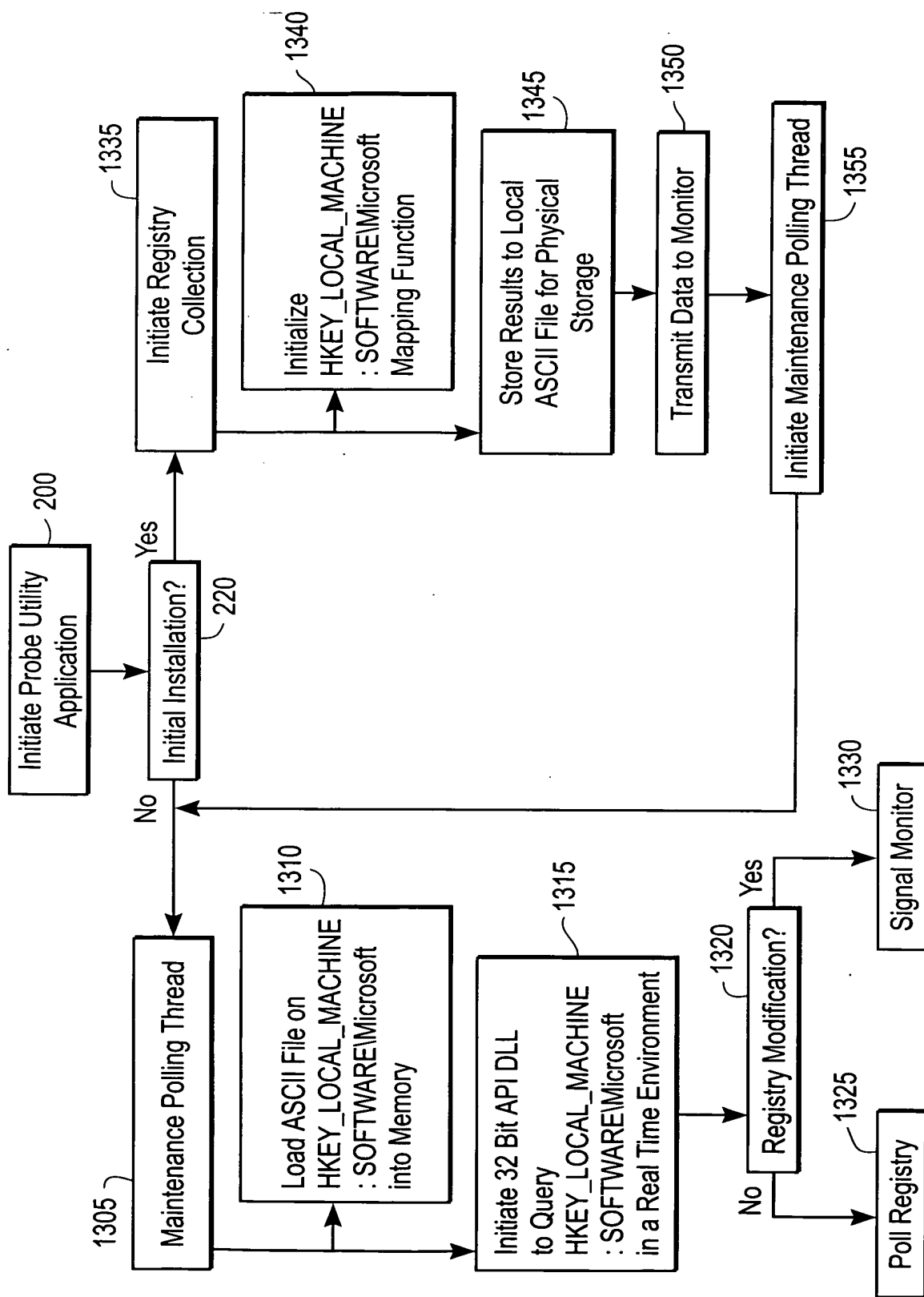


FIG. 13

FO/220" T58/2860

System And Method For Real Time Monitoring And Control Of Networked Computers

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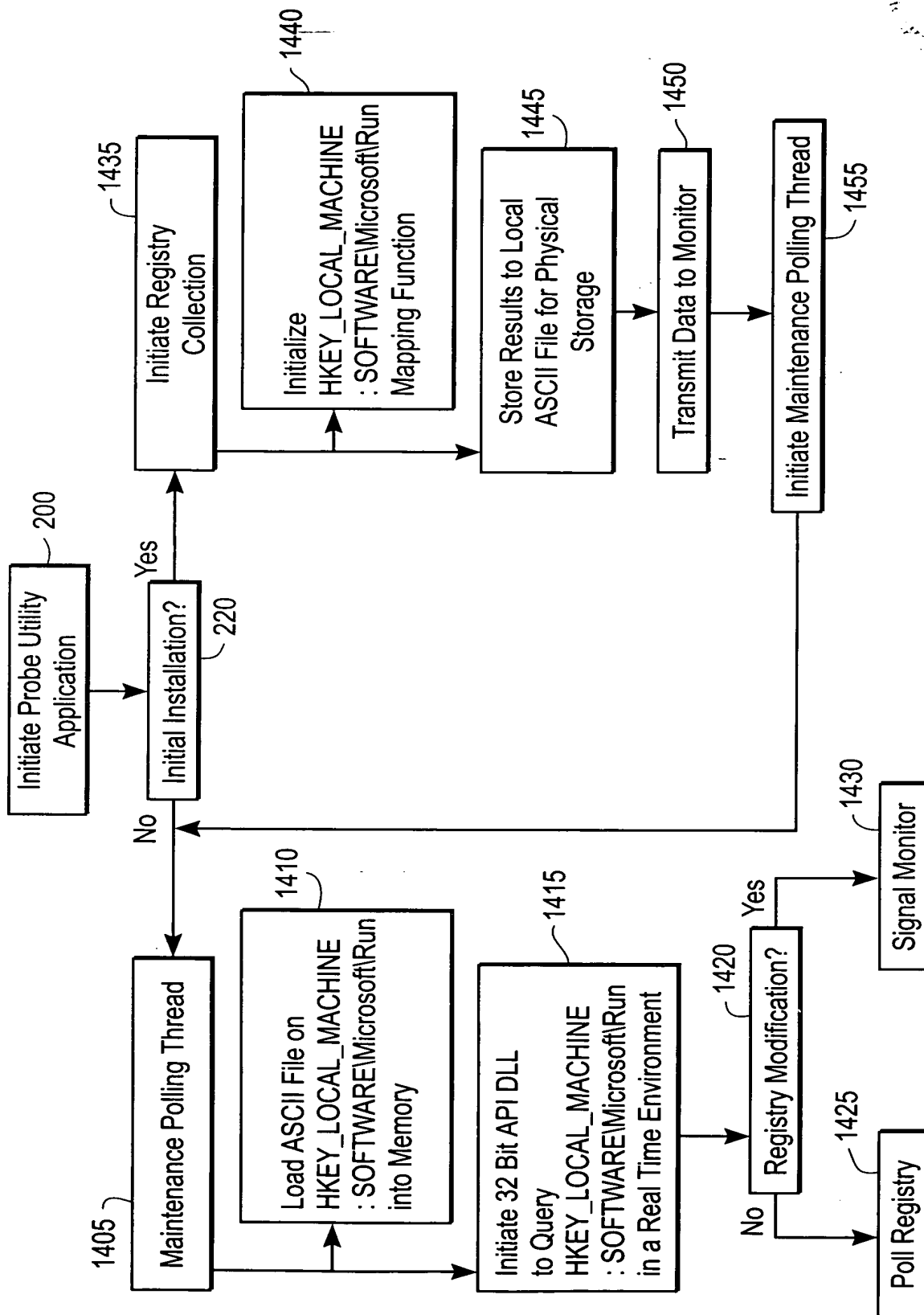


FIG. 14

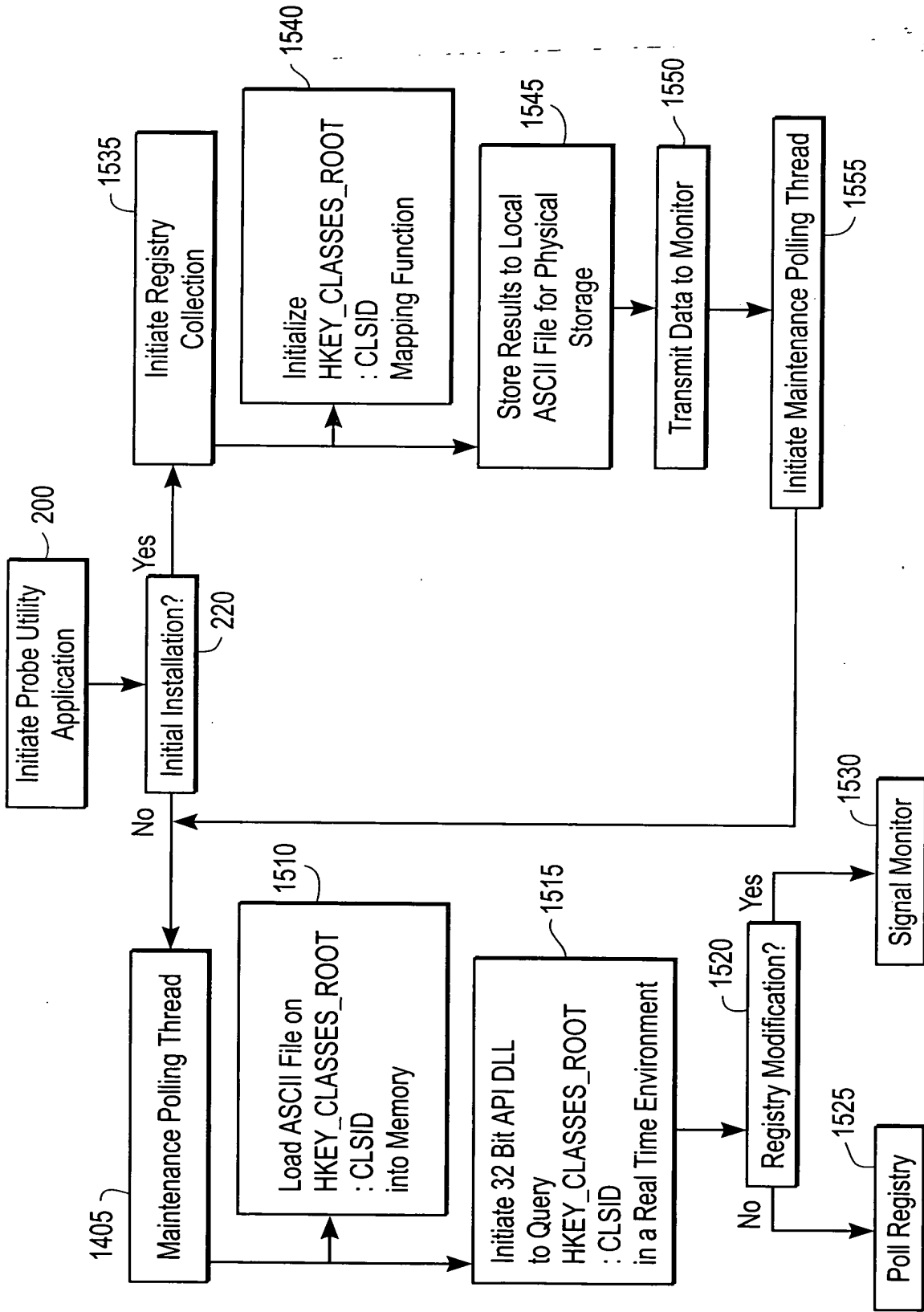


FIG. 15

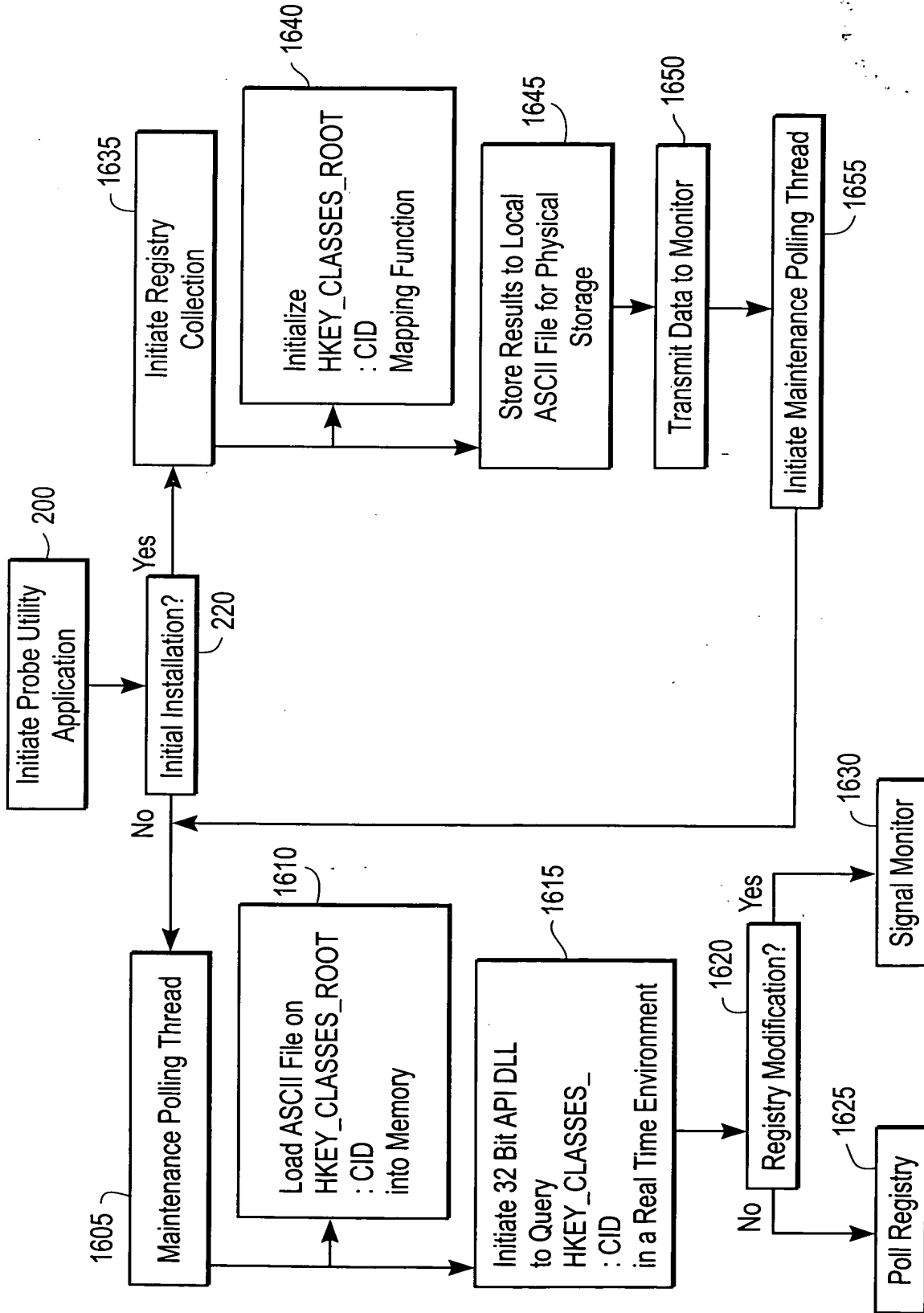


FIG. 16

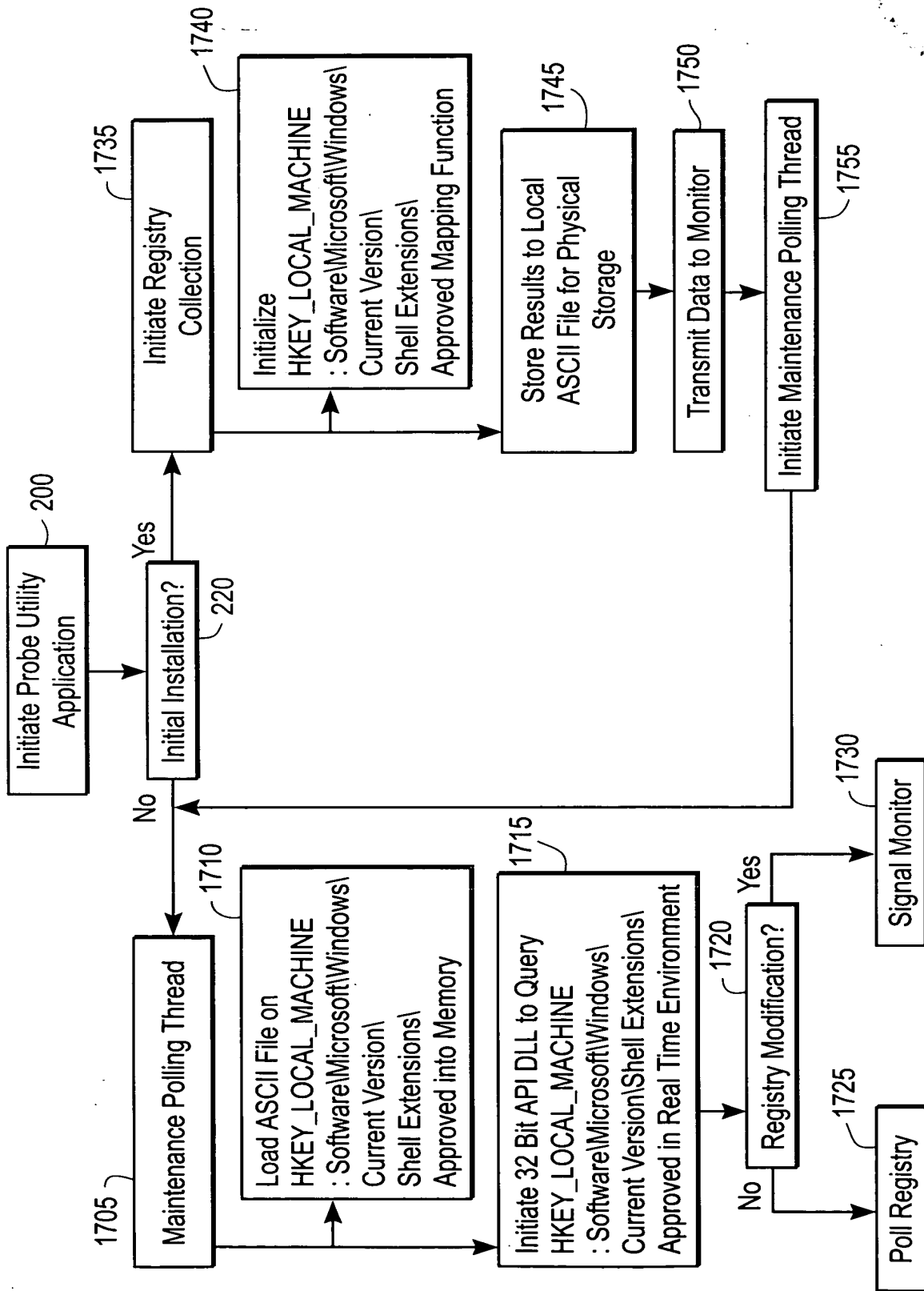


FIG. 17

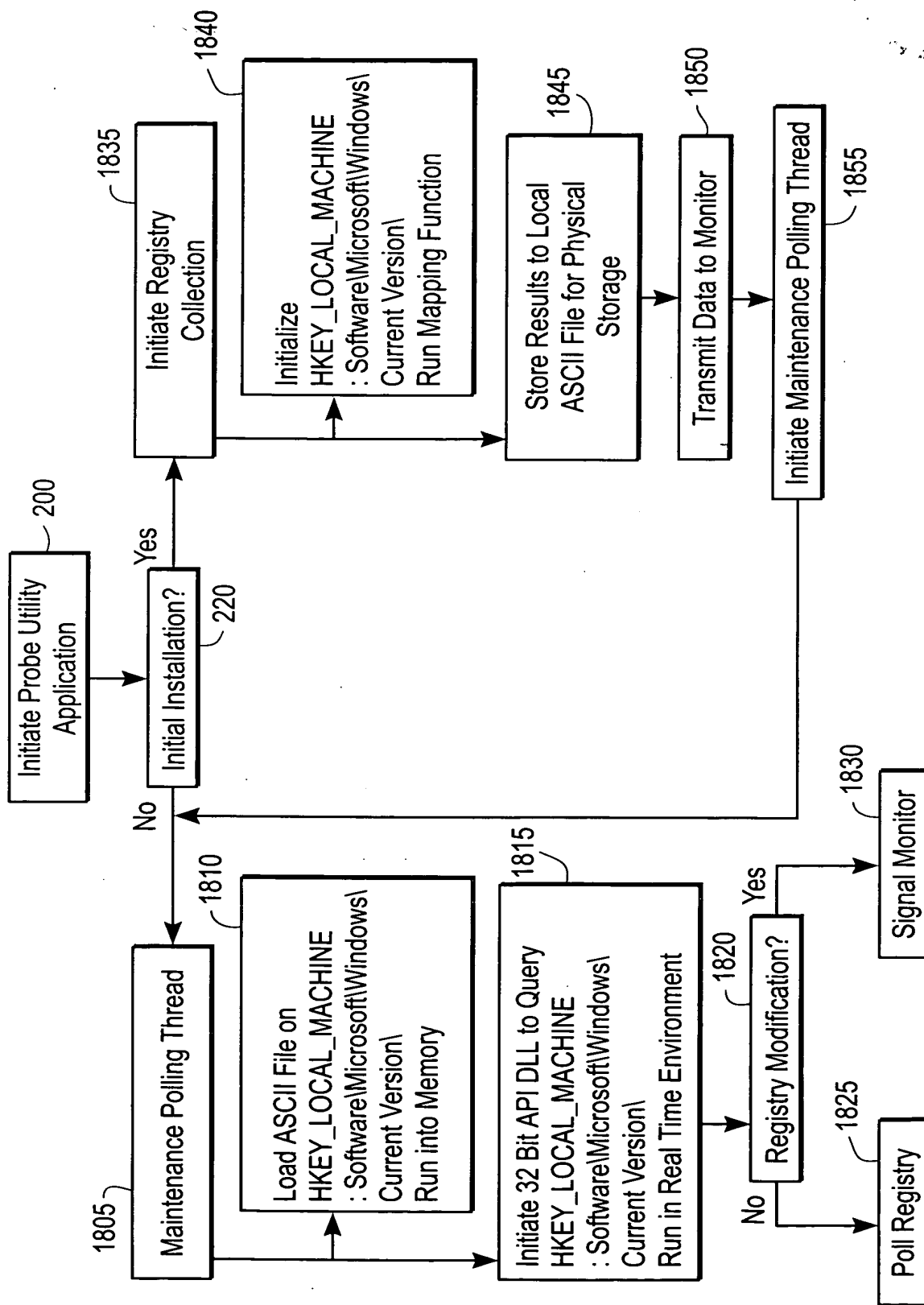


FIG. 18

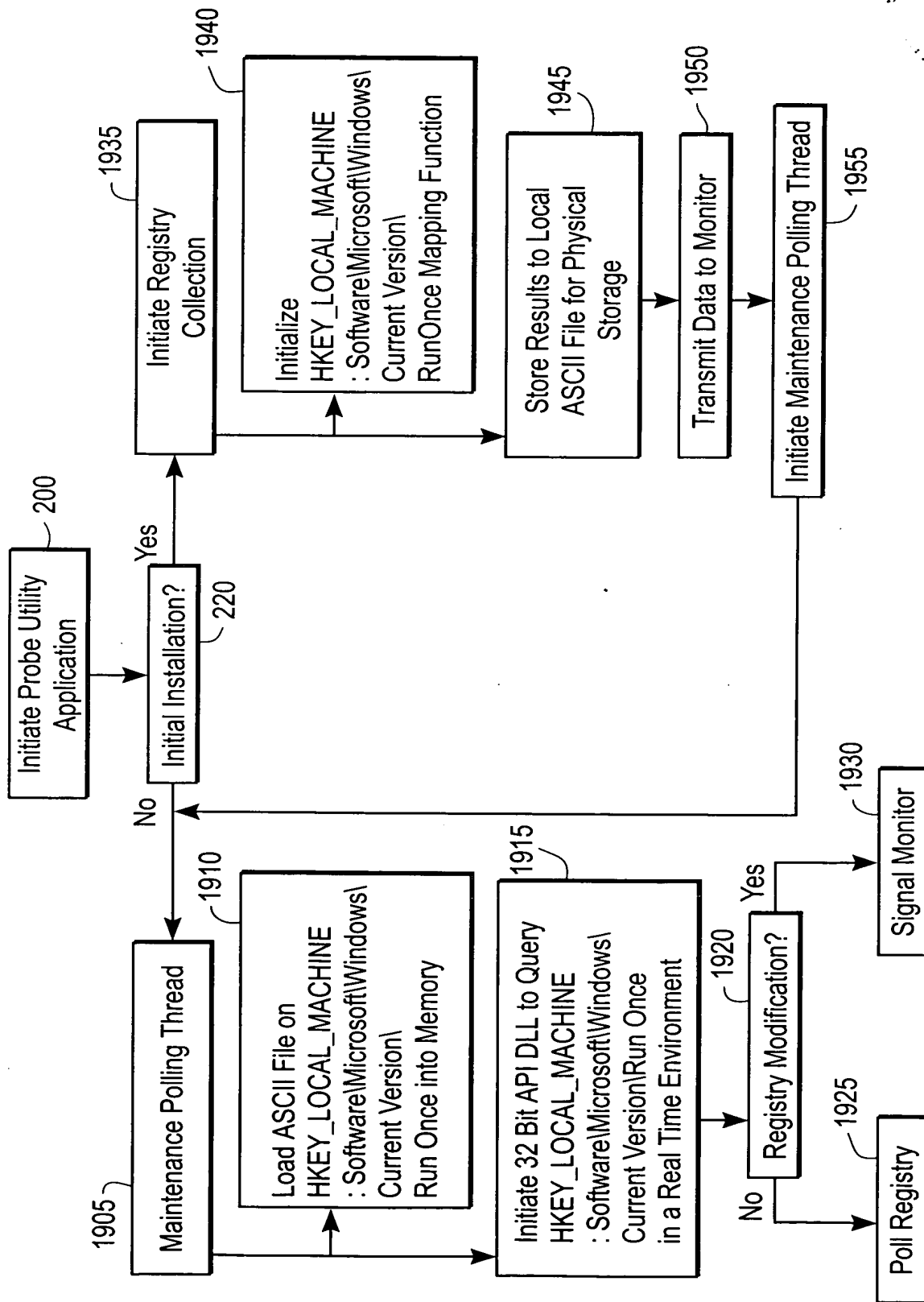


FIG. 19

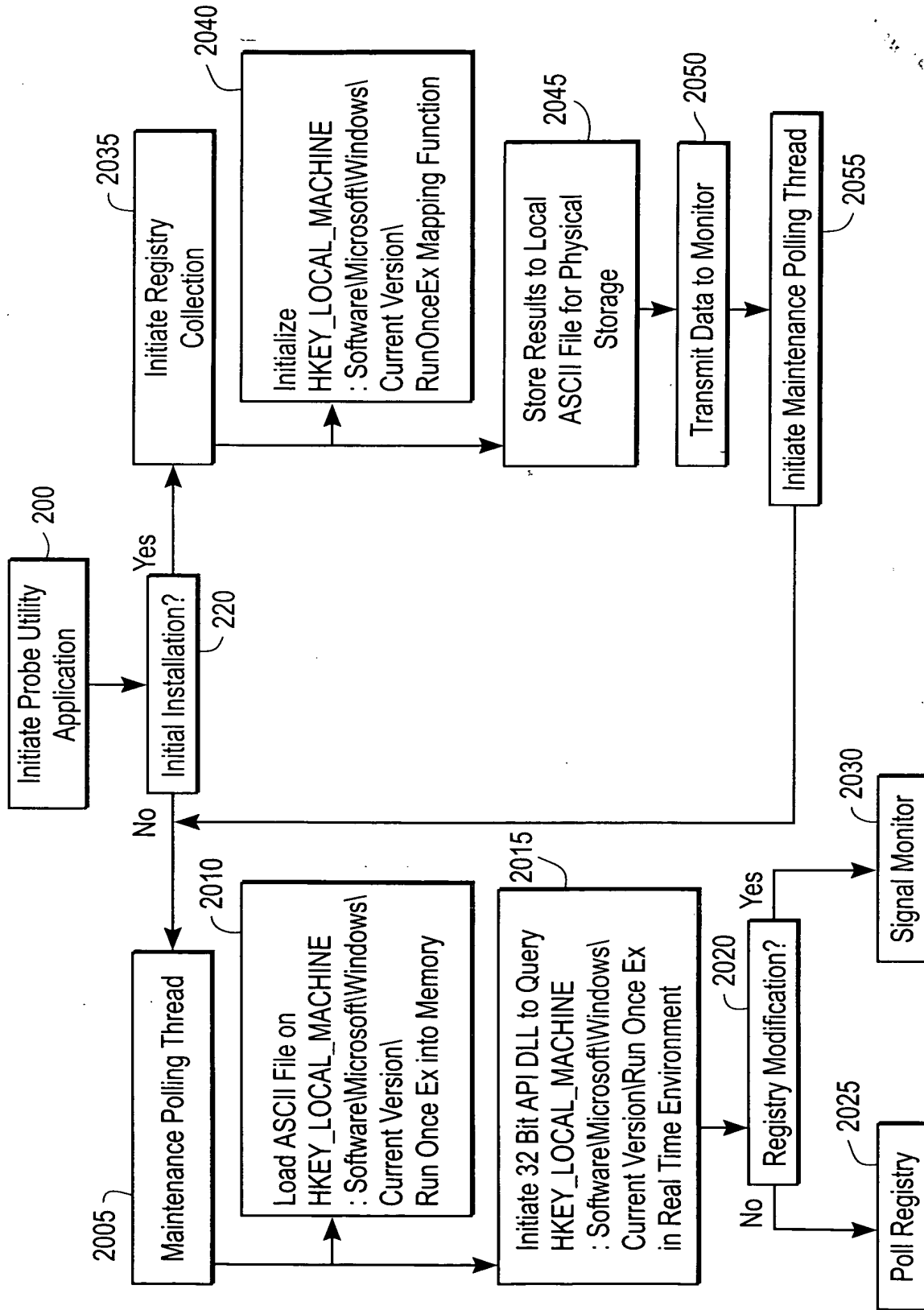


FIG. 20

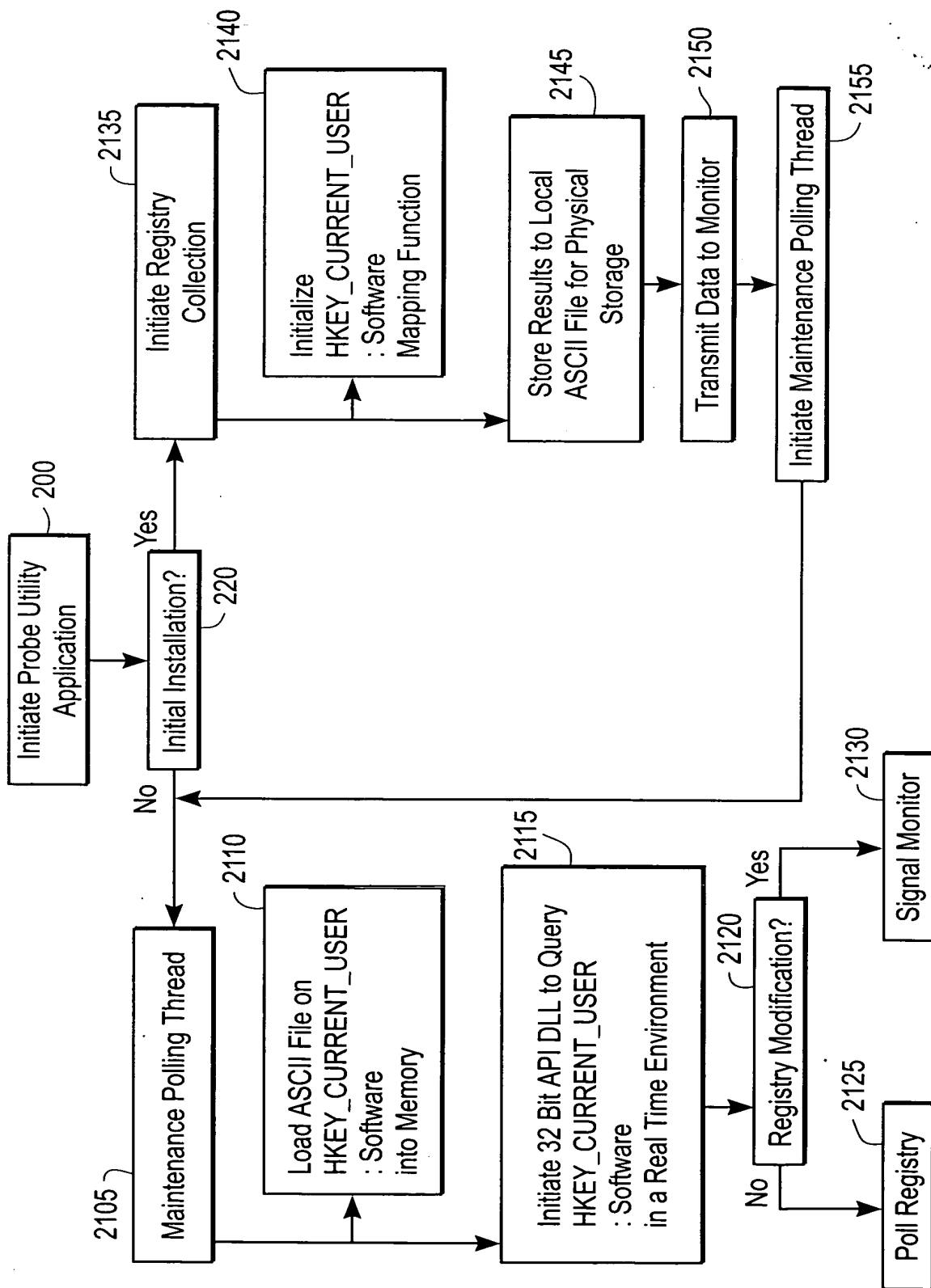
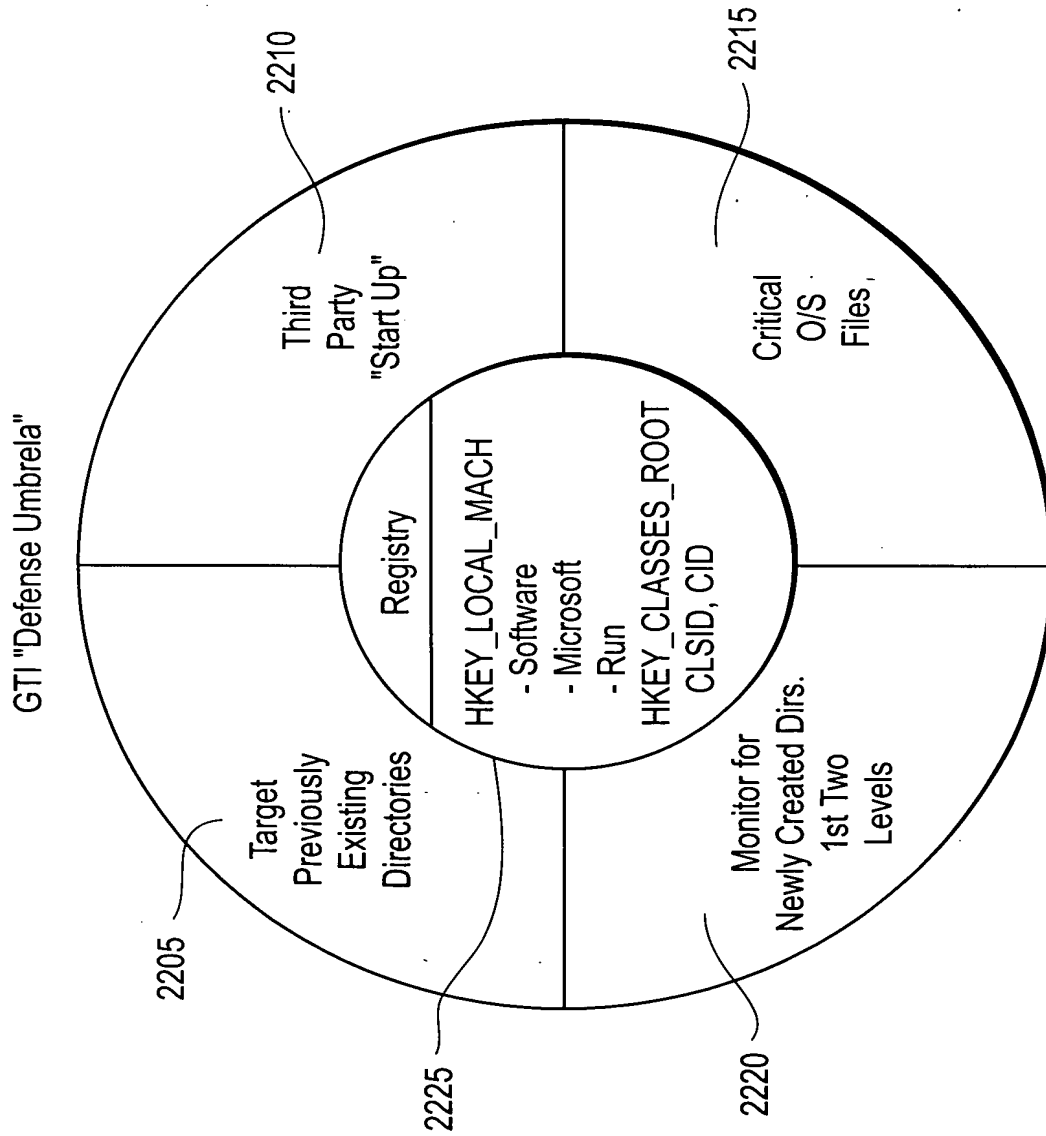


FIG. 21



"Guarded State" of the Complete PC

FIG. 22

FIG. 22

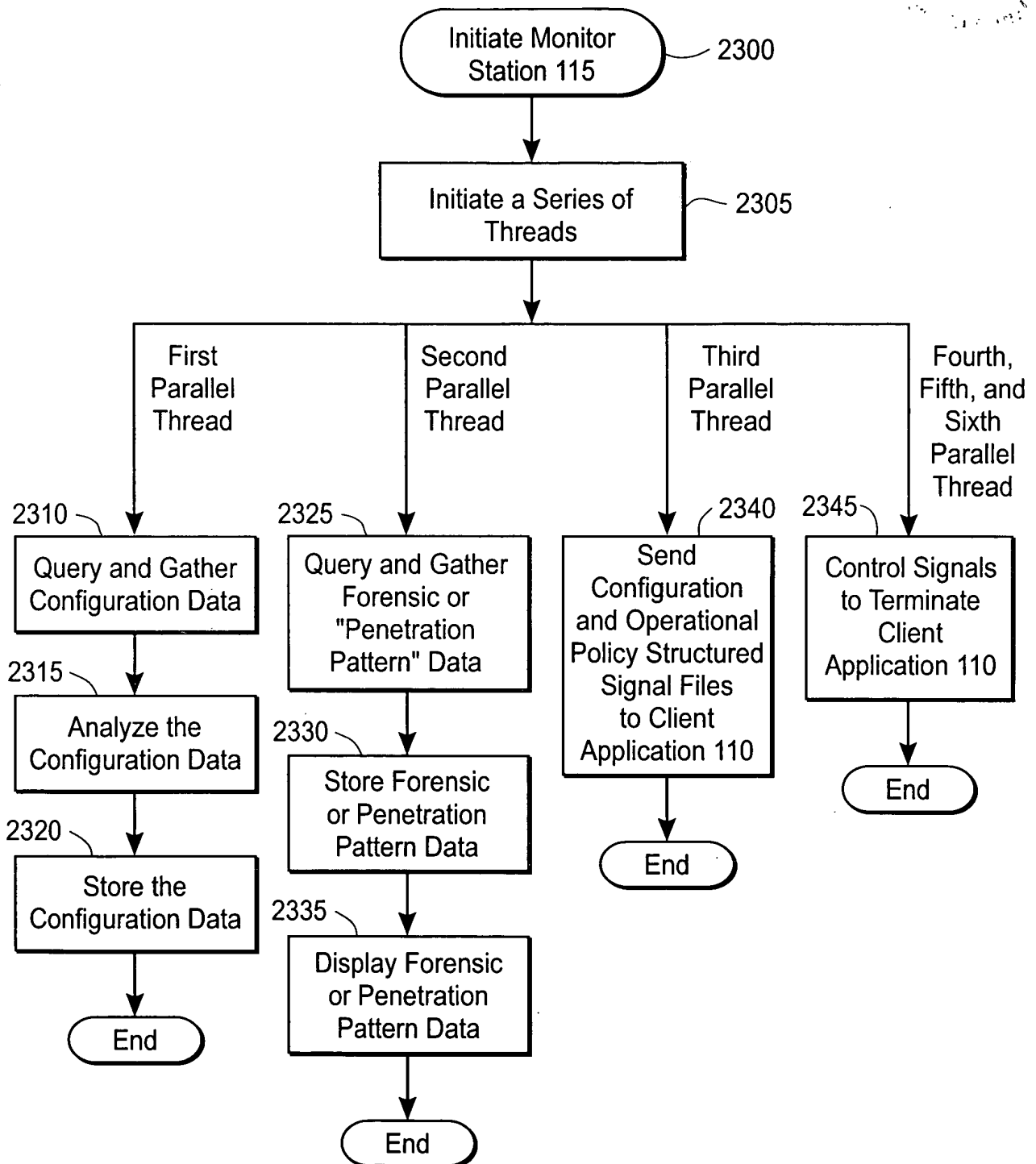


FIG. 23

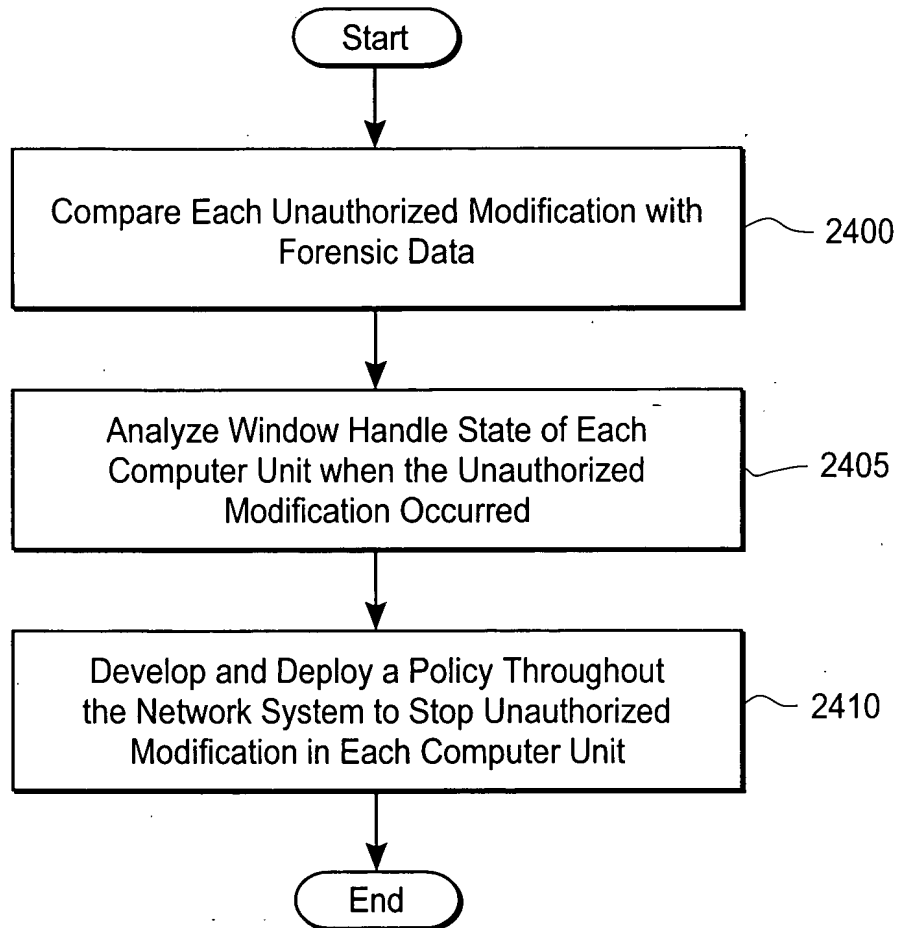


FIG. 24